

ActiveP4: Enabling Active Networking on Programmable Switch Hardware Rajdeep Das / Alex Snoeren





## **In-Network** Functionality





## Emerging Hardware

Programmer writes P4 program



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Performs Computation / Storage







### Limitations

- Deployability & Flexibility:
  - Requires intervention from network operators.
- Multi-tenancy:
  - Applications can't dynamically share switch.
- Manageability:
  - Resources statically assigned to applications.

















### Program Execution



RMT Programmable Switch





## Example: Object Cache





### Active

#### Program



\*Assumes cache is pre-populated with objects.

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## Memory Management



\*Assumes cache is pre-populated with objects.





# Benefits of ActiveP4

- KV-Store Application
- In-Switch Cache
- Flow of Object Requests
- T=3 seconds: introduce cache
- T=6+ seconds: introduce 3 apps





## Dynamic Reallocation

- Huge latency benefits
- Memory allocated equally
- Latencies converge





### Expressibility

- **Operations:** arithmetic ops, logical ops, hashing, ...
- Control flow: branching, loops.
- Memory: direct access and associative.
- Examples:







Cache

Load Balancer

**Database Joins** 





# Work In Progress

- Efficient resource management
  - Memory allocation policies
- Efficient network goodput
  - Cache programs on the switch
  - Invoke programs stored on the switch



